

501.40692X00

2135/5

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Ko Miyazaki et al

Serial No.: 09/964,490

Filed: September 28, 2001

For: FABRICATION METHOD OF SEMICONDUCTOR

INTEGRATED CIRCUIT DEVICE

Art Unit: 2125

Examiner: Shechtman, S.

## <u>UNDER 37 CFR 1.97 & 1.98</u>

Commissioner For Patents P.O. Box 1450 Alexandria, VA 22313-1450

February 1, 2005

Sir:

In the matter of the above-identified application, applicant(s) is/are submitting herewith a copy of the document listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted before the mailing date of either a final action or a notice of allowance and is accompanied by the fee of \$180.00 set forth in 37 CFR 1.17(p).

To the extent the documents listed on the attached form equivalent to Form PTO-1449 are not in the English language, the requirement of 37 CFR

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1.98(a)(3) for a concise explanation of the relevance is satisfied by the discussion of these documents in the specification.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (Case: 501.40692X00), and please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

Alan E. Schiavelli

Registration No. 32,087

(703) 312-6600 Attachments

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